

PAH200S48-*

QUALITY TEST DATA



POWER MODULE

DRAWING NO. : PA554-53-01			
DLJ QA	NLS R&D		
APPROVED	PREPARED	CHECKED	APPROVED
<i>[Signature]</i>	<i>[Signature]</i> 24/7/2000	<i>[Signature]</i> 24/7/00	<i>[Signature]</i> 24/7/00
DATE : 4/5-p/00	DATE ISSUE : 24 July 00		

NEMIC-LAMBDA (S) PTE LTD

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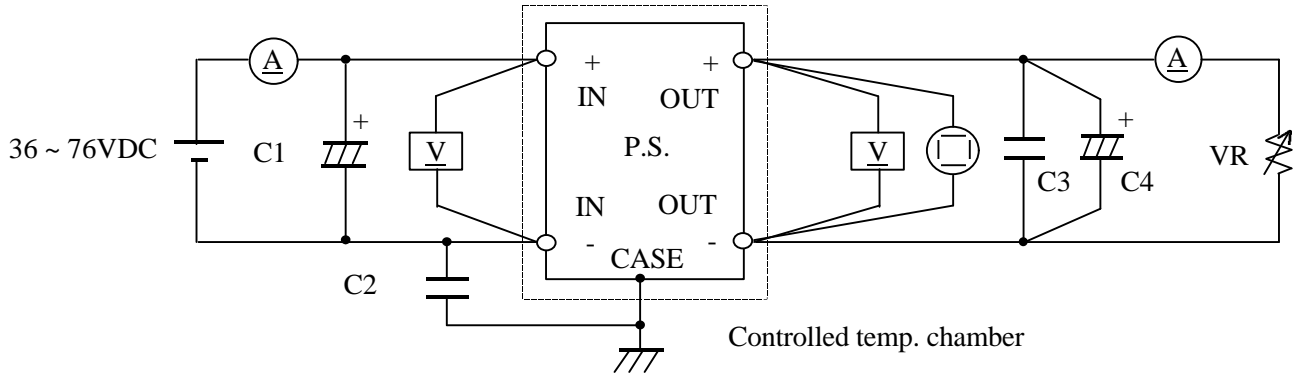
Terminology used

Vin - Input Voltage	Iout - Output Current
Vout - Output Voltage	Tp - Base-Plate Temperature
Iin - Input Current	Tr - Load Rise-Time
	Tf - Load Fall-Time

1. EVALUATION METHOD

1 - 1 Circuit used for determination

(1) Steady state data



(i) $T_p = -20^{\circ}\text{C} \sim 100^{\circ}\text{C}$

C1 : 33 μF Electrolytic Capacitor

C2 : 4700pF Ceramic Capacitor

C3 : 1 μF Ceramic Capacitor

C4 : 12V&15V - 1000 μF Electrolytic Capacitor

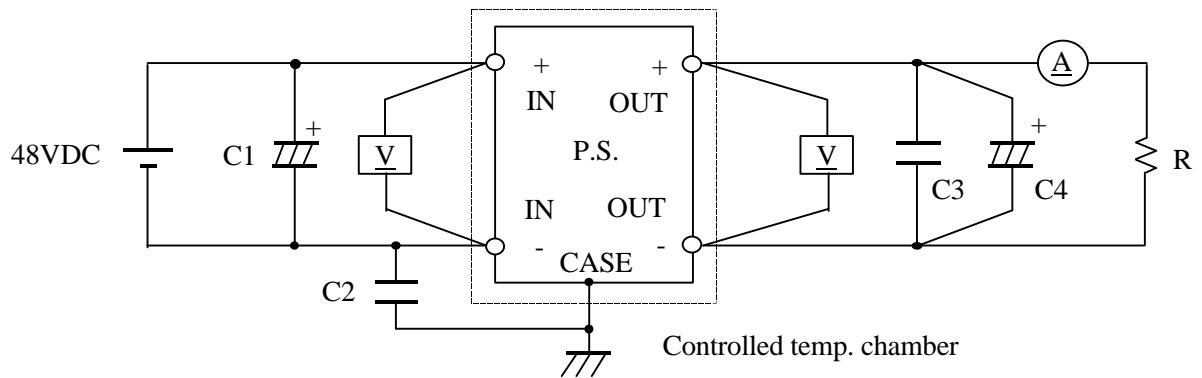
24V&28V - 470 μF Electrolytic Capacitor

(ii) $T_p = -40^{\circ}\text{C} \sim 100^{\circ}\text{C}$

C1 : 33 μF Ceramic Capacitor or equivalent capacitor such as 100V 6.8 μF x 5 pcs

C4 : 2 pieces of the above recommended value

(2) Warm up voltage drift characteristics



C1 : 33 μF Electrolytic Capacitor

C2 : 4700pF Ceramic Capacitor

C3 : 1 μF Ceramic Capacitor

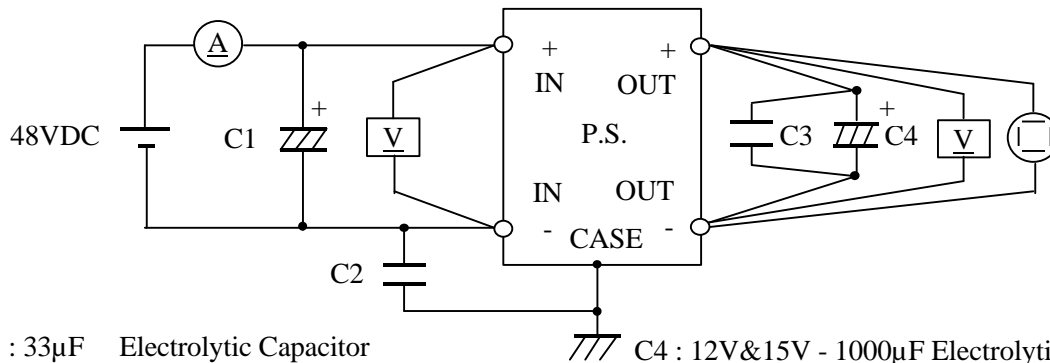
C4 : 12V&15V - 1000 μF Electrolytic Capacitor

24V&28V - 470 μF Electrolytic Capacitor

(3) Over current protection (O.C.P.) characteristics

Same as steady state data

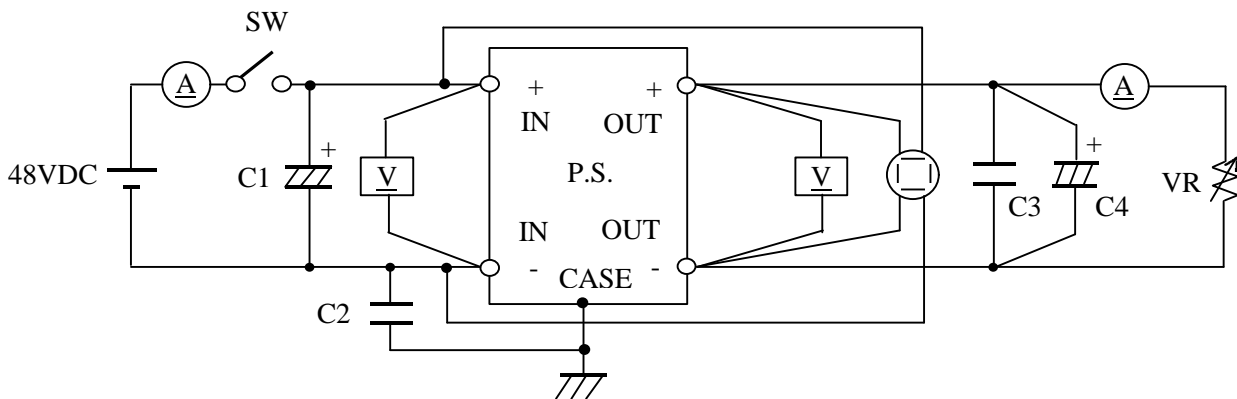
(4) Over voltage protection (O.V.P.) characteristics



C1 : 33 μ F Electrolytic Capacitor
 C2 : 4700pF Ceramic Capacitor
 C3 : 1 μ F Ceramic Capacitor

C4 : 12V&15V - 1000 μ F Electrolytic Capacitor
 24V&28V - 470 μ F Electrolytic Capacitor

(5) Output rise characteristics



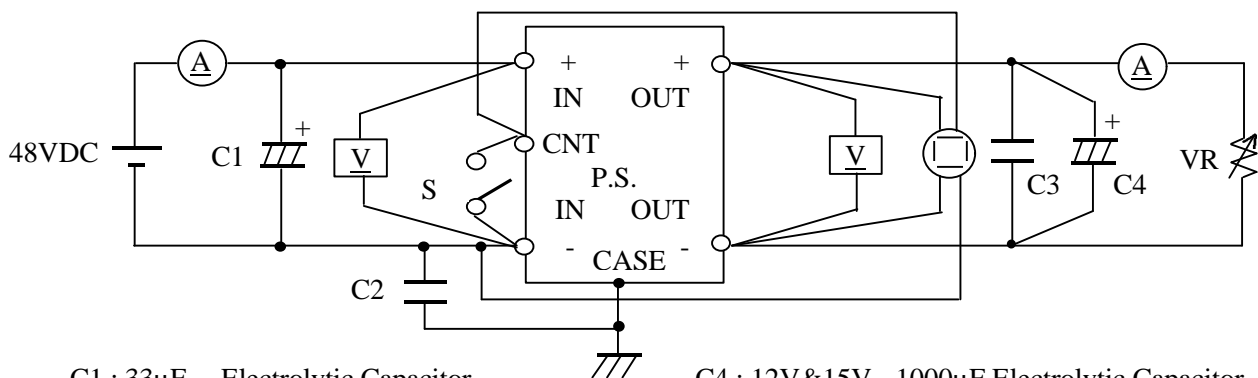
C1 : 33 μ F Electrolytic Capacitor
 C2 : 4700pF Ceramic Capacitor
 C3 : 1 μ F Ceramic Capacitor

C4 : 12V&15V - 1000 μ F Electrolytic Capacitor
 24V&28V - 470 μ F Electrolytic Capacitor

(6) Output fall characteristics

Same as Output rise characteristics

(7) Output rise characteristics with on/off control

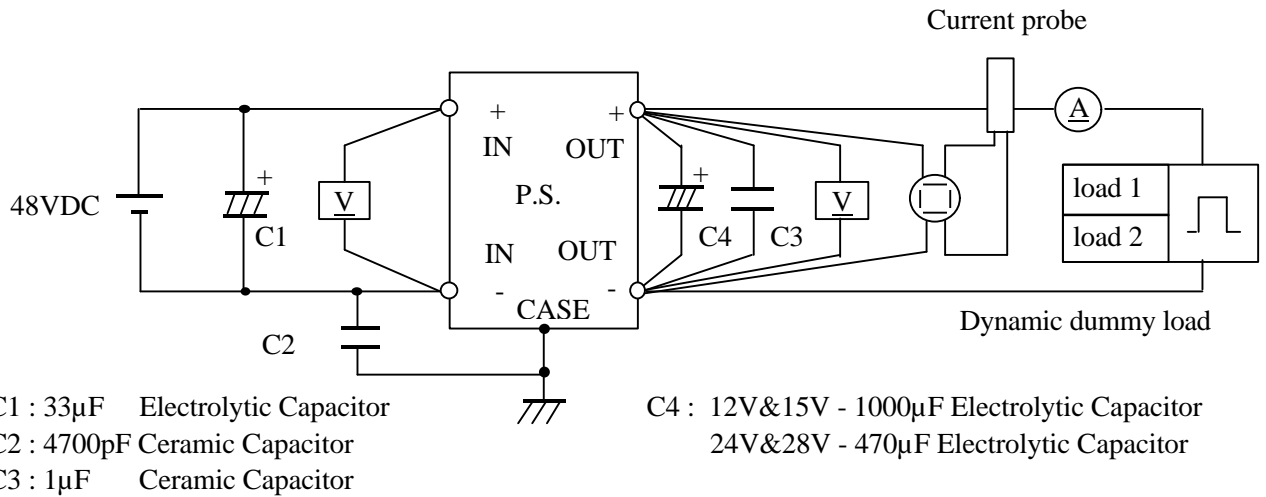


C1 : 33 μ F Electrolytic Capacitor
 C2 : 4700pF Ceramic Capacitor
 C3 : 1 μ F Ceramic Capacitor

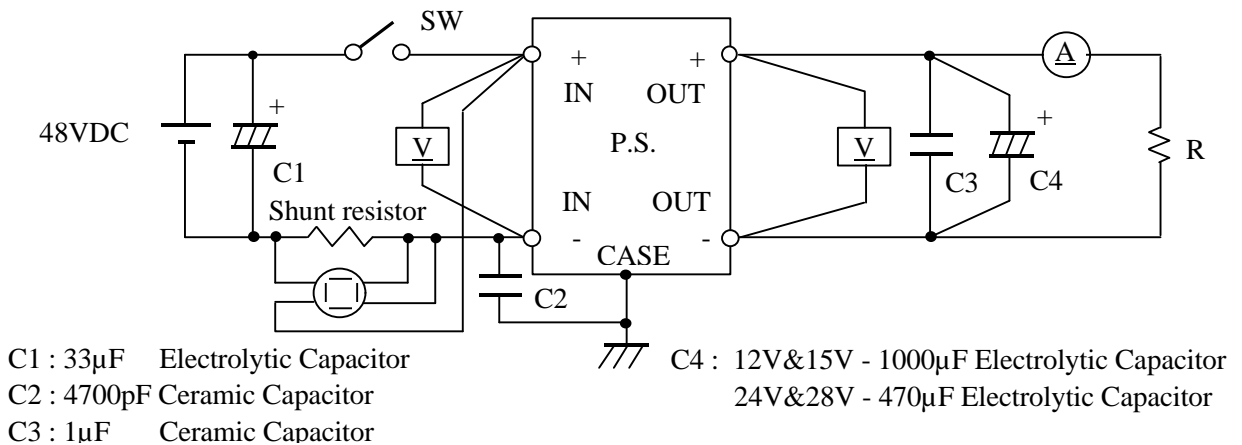
C4 : 12V&15V - 1000 μ F Electrolytic Capacitor
 24V&28V - 470 μ F Electrolytic Capacitor

(8) Output fall characteristics with on/off control
 Same as Output rise characteristics with on/off control

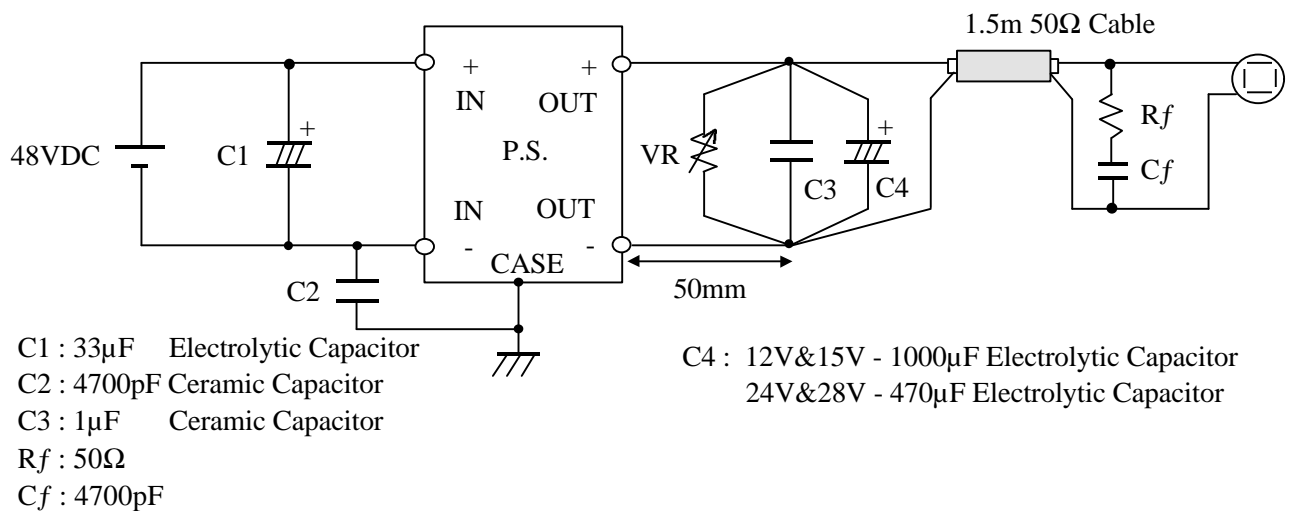
(9) Dynamic load response characteristics



(10) Inrush current waveform

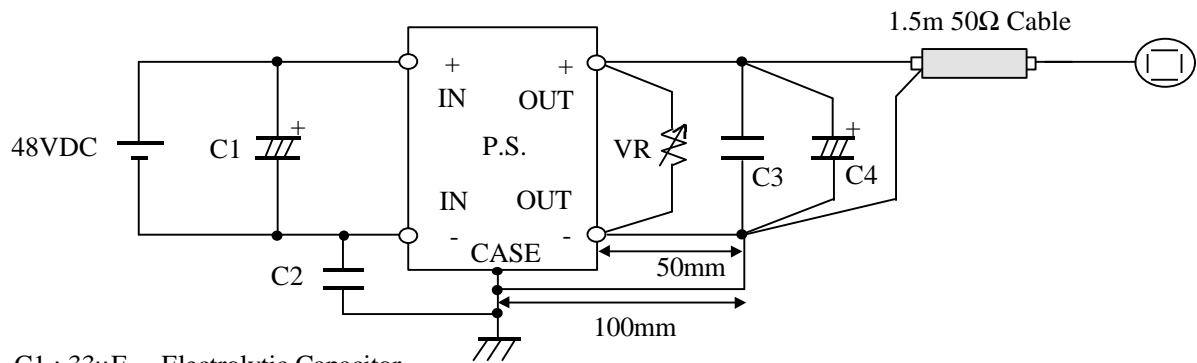


(11) Output-ripple , noise waveform
 NORMAL MODE (EIAJ Standard RC-9002A)



(11) Output-ripple , noise waveform

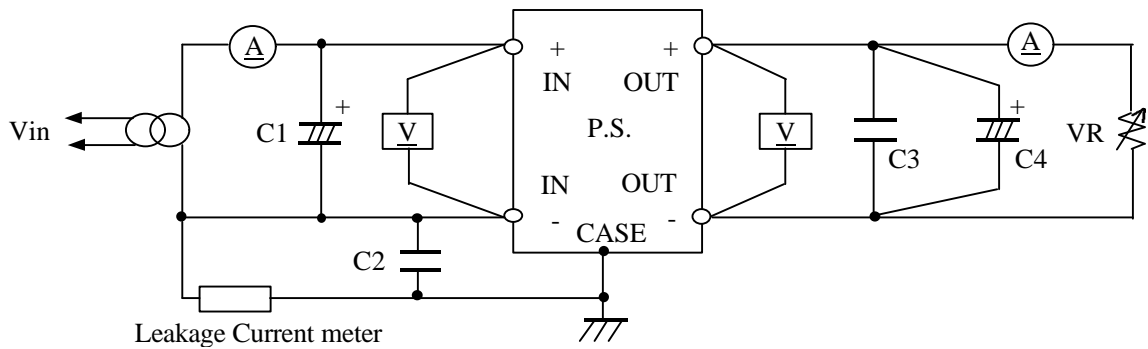
NORMAL + COMMON MODE



C1 : 33µF Electrolytic Capacitor
 C2 : 4700pF Ceramic Capacitor
 C3 : 1µF Ceramic Capacitor

C4 : 12V&15V - 1000µF Electrolytic Capacitor
 24V&28V - 470µF Electrolytic Capacitor

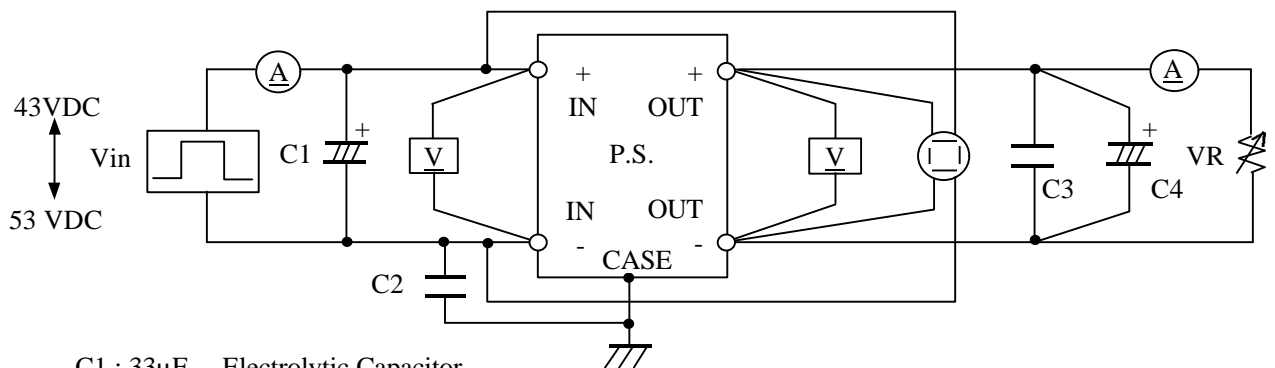
(12) Leakage current characteristics



C1 : 33µF Electrolytic Capacitor
 C2 : 4700pF Ceramic Capacitor
 C3 : 1µF Ceramic Capacitor

C4 : 12V&15V - 1000µF Electrolytic Capacitor
 24V&28V - 470µF Electrolytic Capacitor

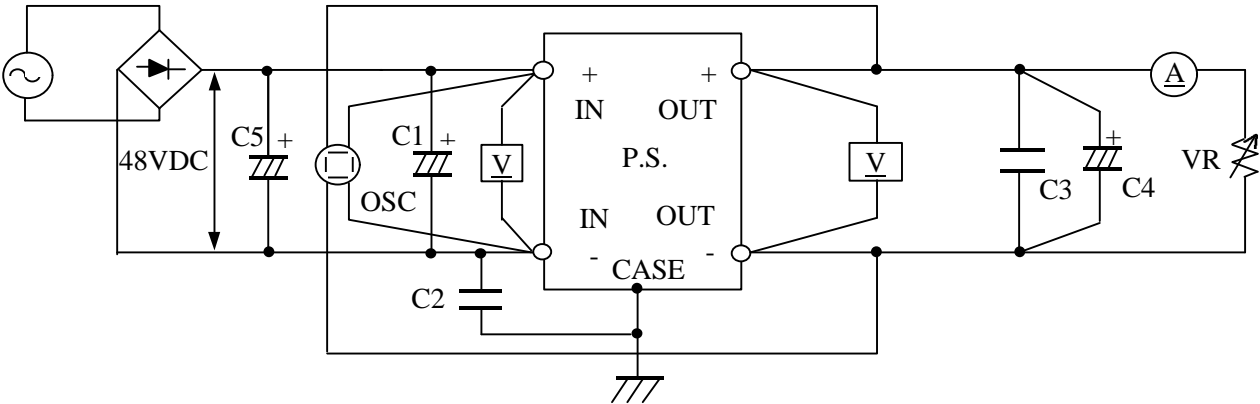
(13) Dynamic line characteristics



C1 : 33µF Electrolytic Capacitor
 C2 : 4700pF Ceramic Capacitor
 C3 : 1µF Ceramic Capacitor

C4 : 12V&15V - 1000µF Electrolytic Capacitor
 24V&28V - 470µF Electrolytic Capacitor

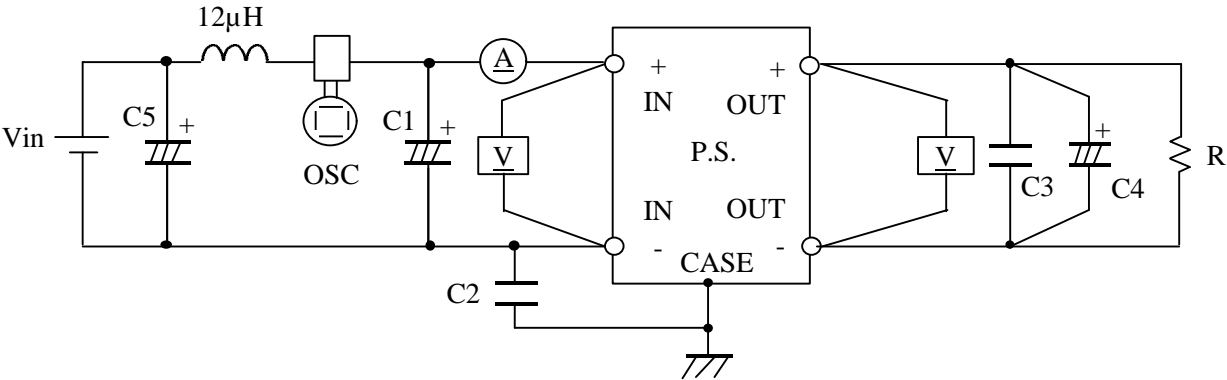
(14) AC input response characteristics



- C1 : 33 μ F Electrolytic Capacitor
- C2 : 4700pF Ceramic Capacitor
- C3 : 1 μ F Ceramic Capacitor
- C5 : 560 μ F Electrolytics Capacitor

- C4 : 12V&15V - 1000 μ F Electrolytic Capacitor
- 24V&28V - 470 μ F Electrolytic Capacitor

(15) Input Reflected current characteristics



- C1 : 33 μ F Electrolytic Capacitor
- C2 : 4700pF Ceramic Capacitor
- C3 : 1 μ F Ceramic Capacitor
- C5 : 220 μ F Elerctrolytic Capacitor

- C4 : 12V&15V - 1000 μ F Electrolytic Capacitor
- 24V&28V - 470 μ F Electrolytic Capacitor

1-2 List of equipment used

No	Description	Manufacturer	Model No.
1	Oscilloscope	TEKTRONIX	2465B
2		HITACHI	V-1050F
3	Digital oscilloscope	YEW	DL2140
4		HITACHI	VC-6041
5	Digital volt meter	IWATSU	VDAC 7411
6	DC ampere meter	YOKOGAWA ELEC.	2051
7	Dynamic dummy load	TAKAMIZAWA	PSA-150D
8	Variable resistive load	MATSUNAGA	44/11 Ω
9	Variable resistive load	MATSUNAGA	2.4/0.6 Ω
10	Controlled temp . chamber	TABAI	PL-2GM
11	Shunt resistor	KUWANO	100mV , 1A
12	Current probe amplifier	TEKTRONIX	TM503
13	Current probe	TEKTRONIX	A6303
14	AC power source / Analyzer	HEWLETT PACKARD	6813A
15	Leakage current tester	SIMPSON	229-2

2. CHARACTERISTICS

2-1 Steady State Data

(1) Regulation - Line and Load, Temperature Drift

12 V

Regulation - Line and Load

Tp = 25°C

Iout \ Vin	36 VDC	48 VDC	76 VDC	Line Regulation (mV)	
	0%	12.0530	12.0540	12.0540	1.0
50%	12.0510	12.0520	12.0520	1.0	0.008%
100%	12.0500	12.0500	12.0510	1.0	0.008%
Load Regulation (mV)	3.0	4.0	3.0		
	0.025%	0.033%	0.025%		

Temperature Drift

Vin = 48VDC

Iout = 100%

Tp	-40°C	25°C	100°C	Temp. Stability	
Vout	12.030V	12.050 V	11.950 V	0.08	0.66%

24 V

Regulation - Line and Load

Tp = 25°C

Iout \ Vin	36 VDC	48 VDC	76 VDC	Line Regulation (mV)	
	0%	24.01	24.003	24.009	7.0
50%	24.004	24.004	23.995	9.0	0.037%
100%	23.987	23.997	23.994	10.0	0.042%
Load Regulation (mV)	23.0	7.0	15.0		
	0.096%	0.029%	0.062%		

Temperature Drift

Vin = 48VDC

Iout = 100%

Tp	-40°C	25°C	100°C	Temp. Stability	
Vout	23.977 V	23.997 V	23.879 V	0.098 V	0.41%

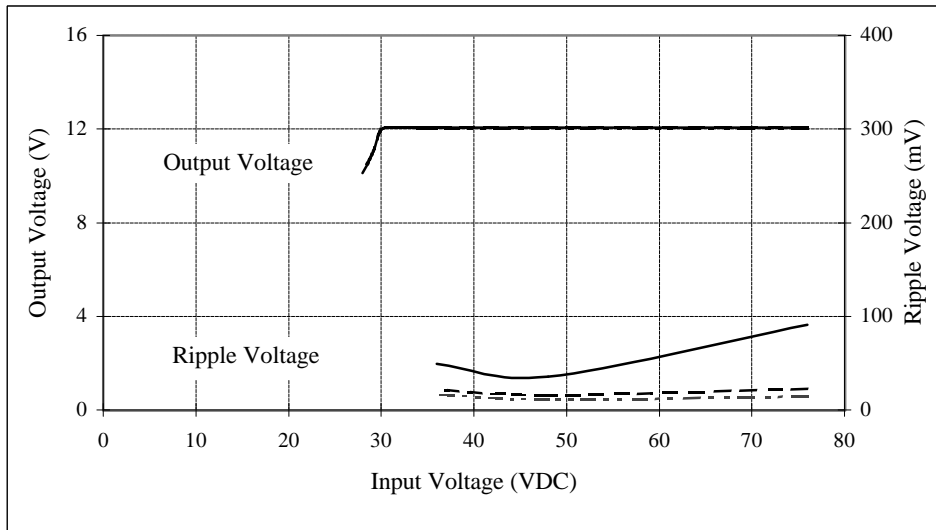
2. CHARACTERISTICS

2-1 Steady State Data

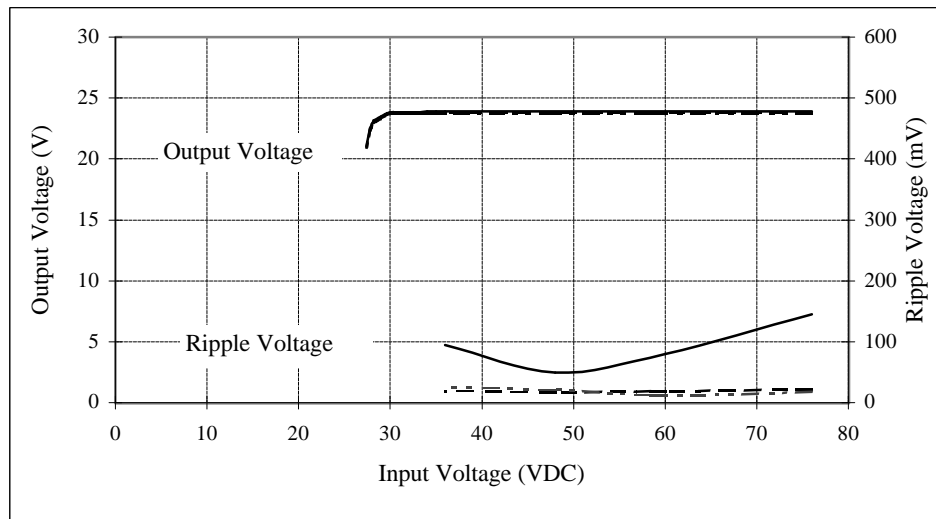
(2) Output Voltage And Ripple Voltage V.S. Input Voltage

Condition : $I_{out} = 100\%$
 $T_p = -40^\circ\text{C}$ ———
 $T_p = 25^\circ\text{C}$ - - - - -
 $T_p = 100^\circ\text{C}$ ·····

12 V



24 V



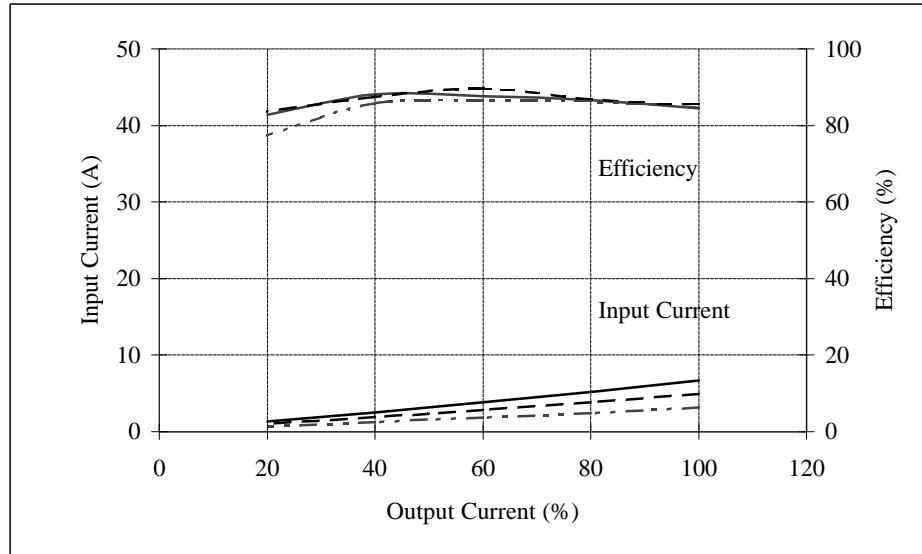
2. CHARACTERISTICS

2-1 Steady State Data

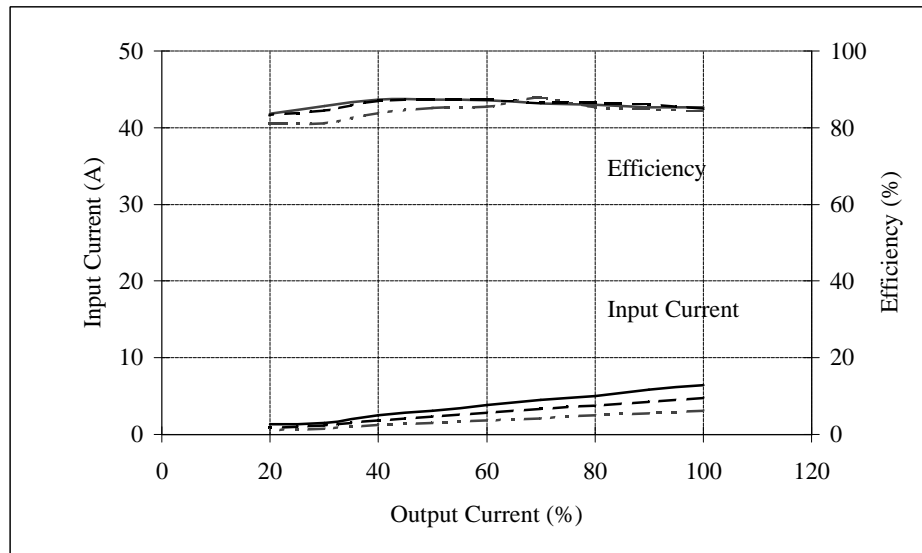
(3) Efficiency And Input Current V.S. Output Current

Condition : $V_{in} = 36\text{VDC}$ ———
 $= 48\text{VDC}$ - - - - -
 $= 76\text{VDC}$ - · - · - ·
 $T_p = 25^\circ\text{C}$

12 V



24 V



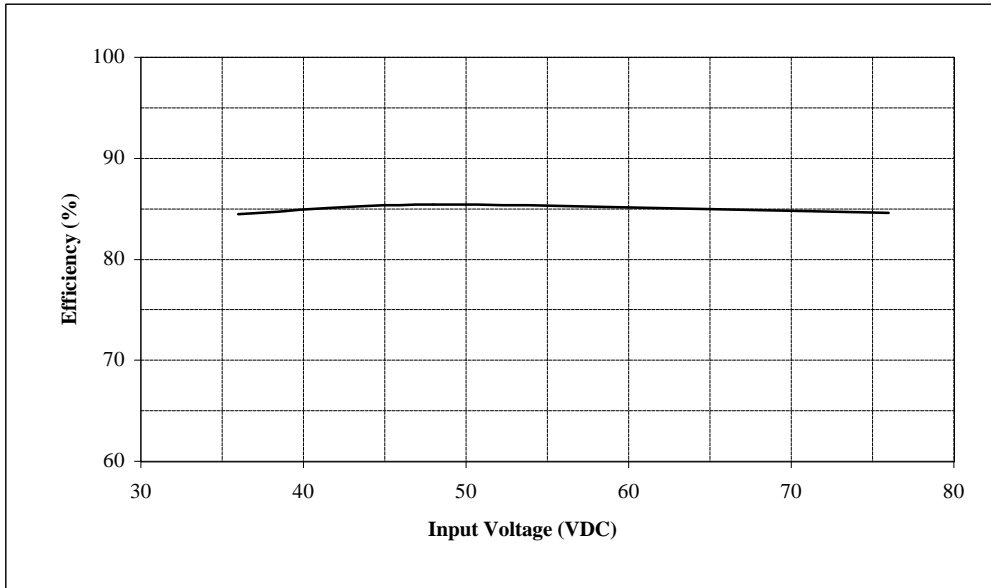
2. CHARACTERISTICS

2-1 Steady State Data

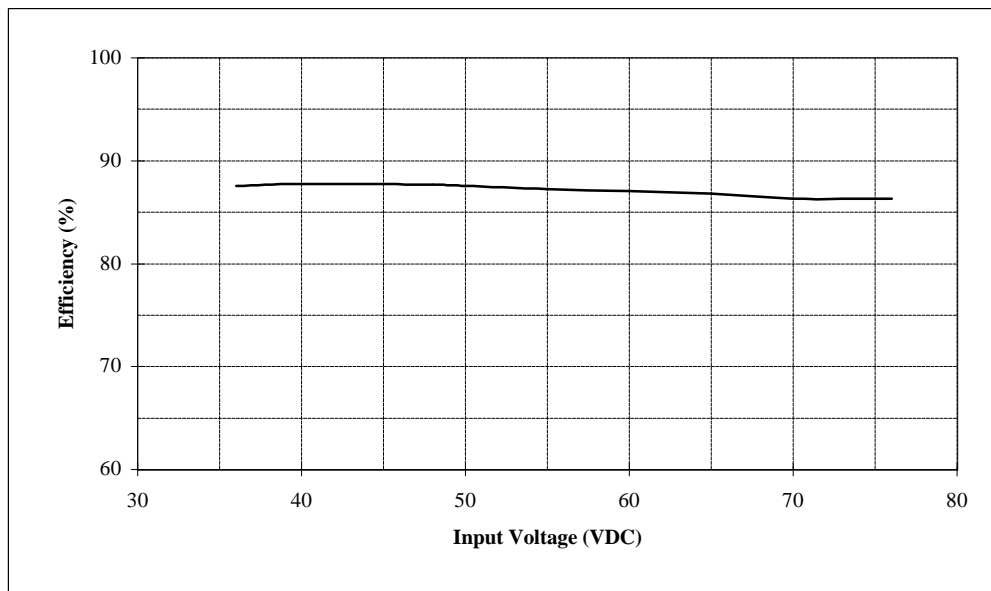
(4) Efficiency v.s. Input Voltage

Condition : $I_{out} = 100\%$
 $T_p = 25^\circ\text{C}$

12 V



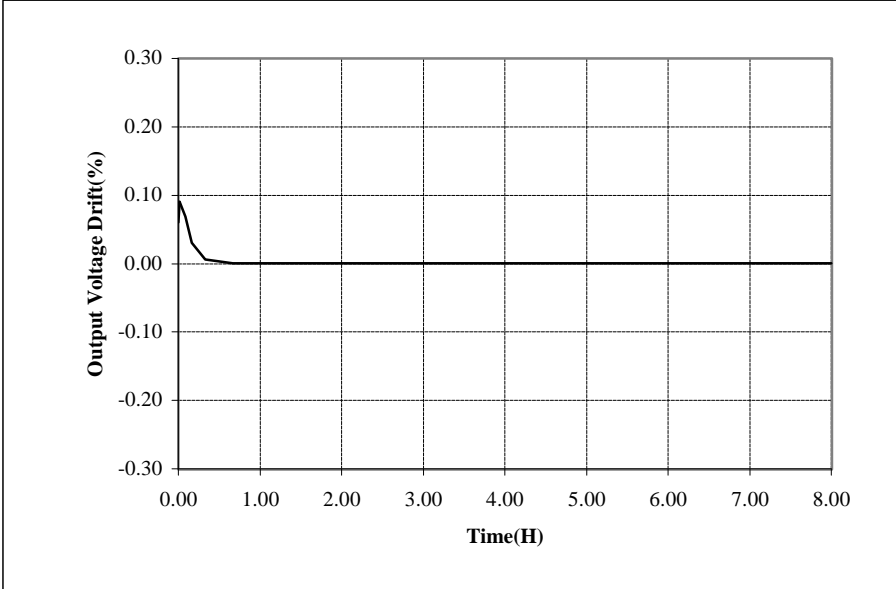
24 V



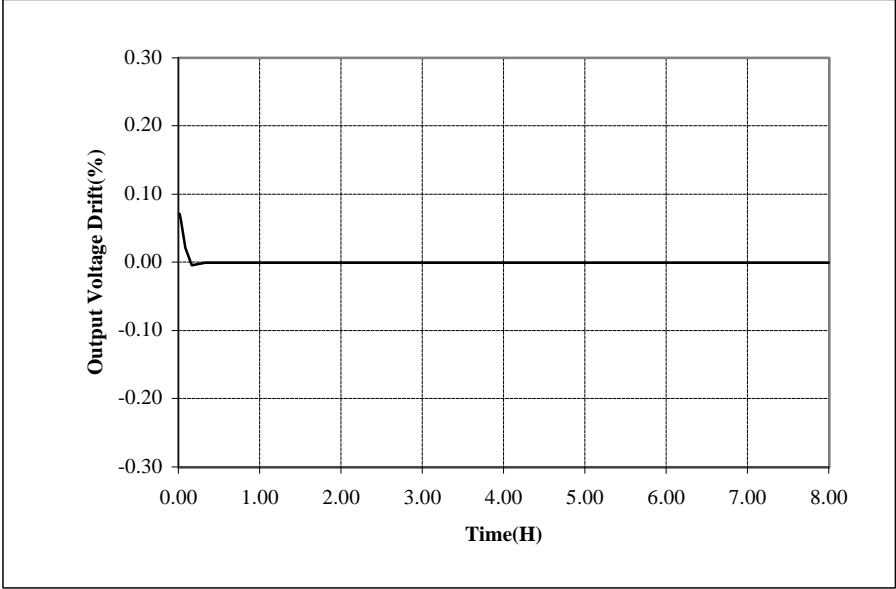
2-2 Warm Up Voltage Drift Characteristics

Condition : $V_{in} = 48 \text{ V DC}$
 $I_{out} = 100 \%$
 $T_p = 25^\circ\text{C}$

12 V



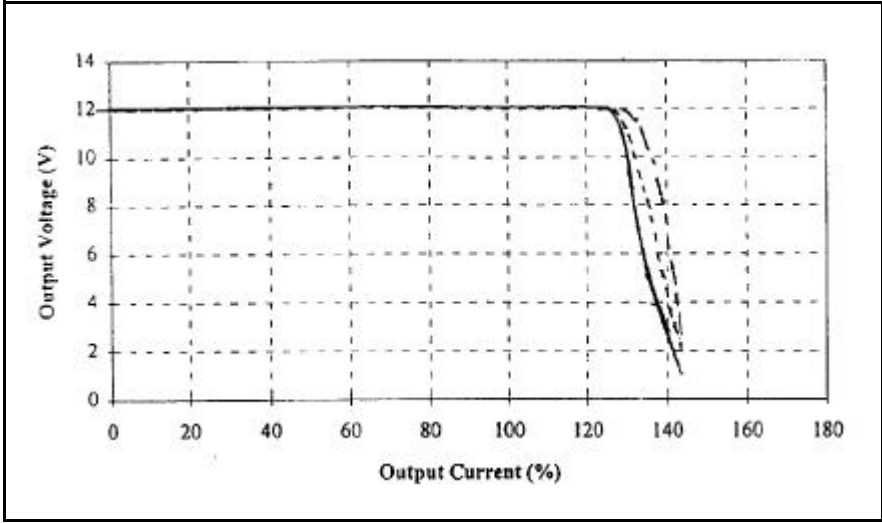
24 V



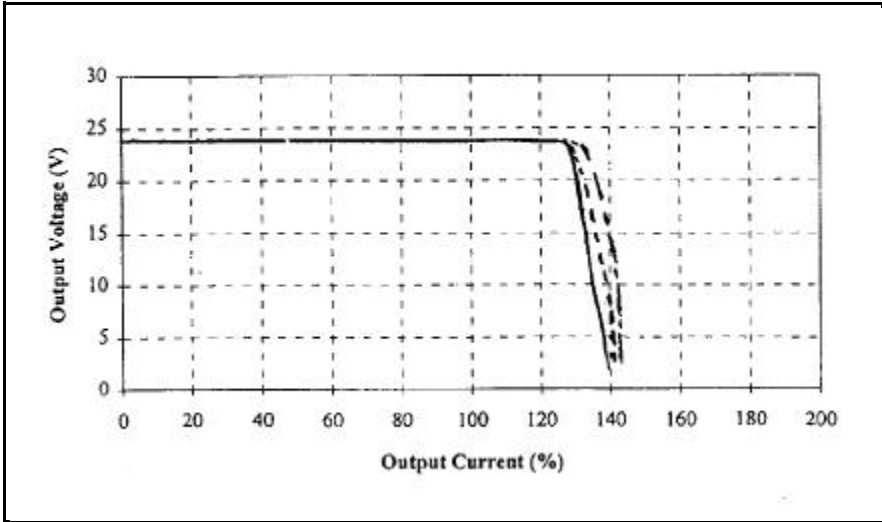
2-3 O.C.P. Charateristics

Condition : Vin = 36 VDC —————
 Vin = 48 VDC - - - - -
 Vin = 76 VDC - · - · - ·
 Tp = 25°C

12 V



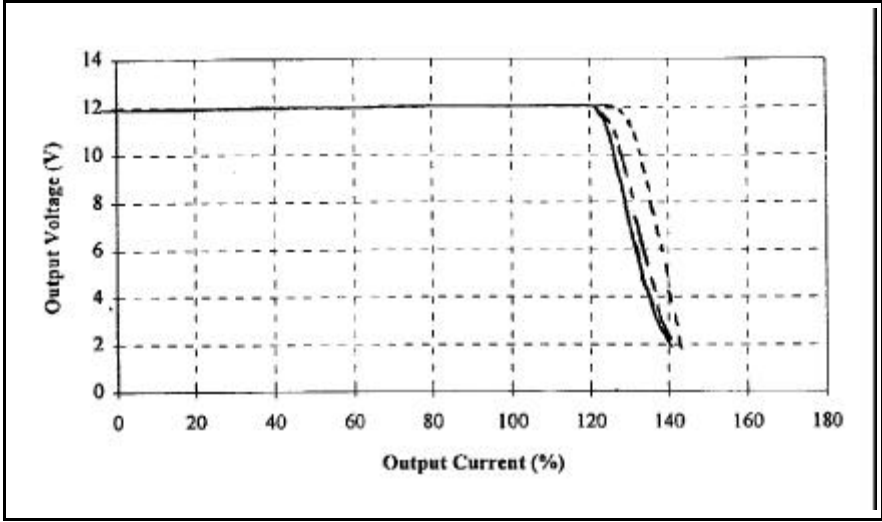
24V



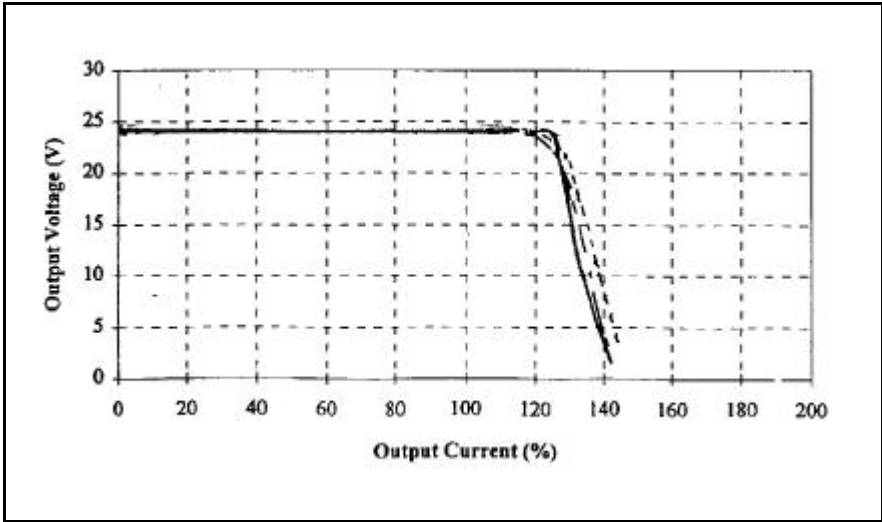
O.C.P. Characteristics

Condition : $T_p = -40^\circ\text{C}$ —————
 $T_p = 25^\circ\text{C}$ - - - - -
 $T_p = 100^\circ\text{C}$ - · - · - ·
 $V_{in} = 48 \text{ VDC}$

12V



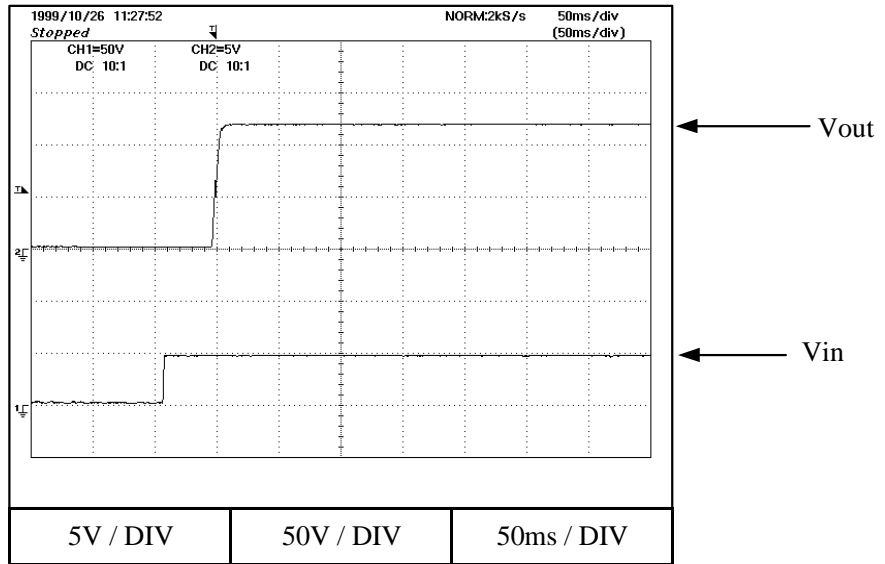
24V



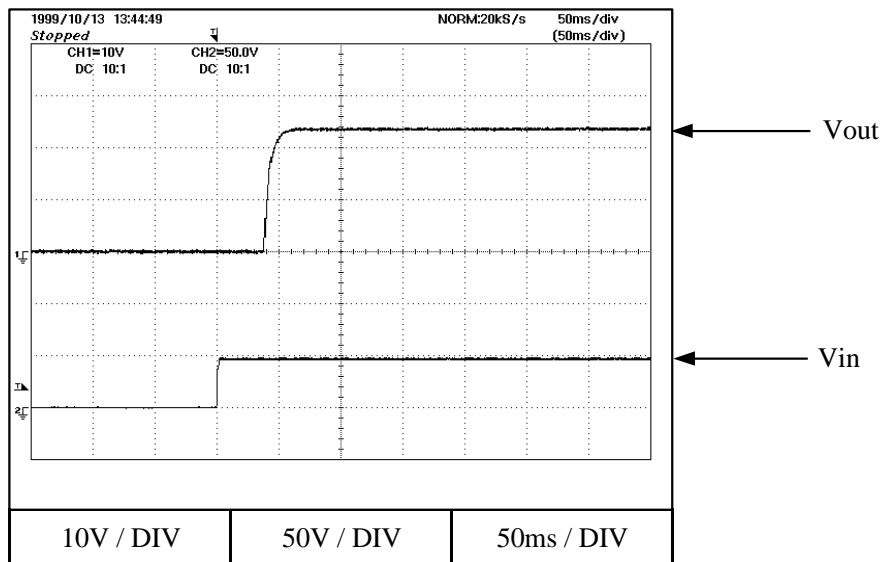
2-5 Output Rise Characteristics

Condition : $V_{in} = 48\text{ V DC}$
 $I_{out} = 0\%$
 $T_p = 25^\circ\text{C}$

12 V



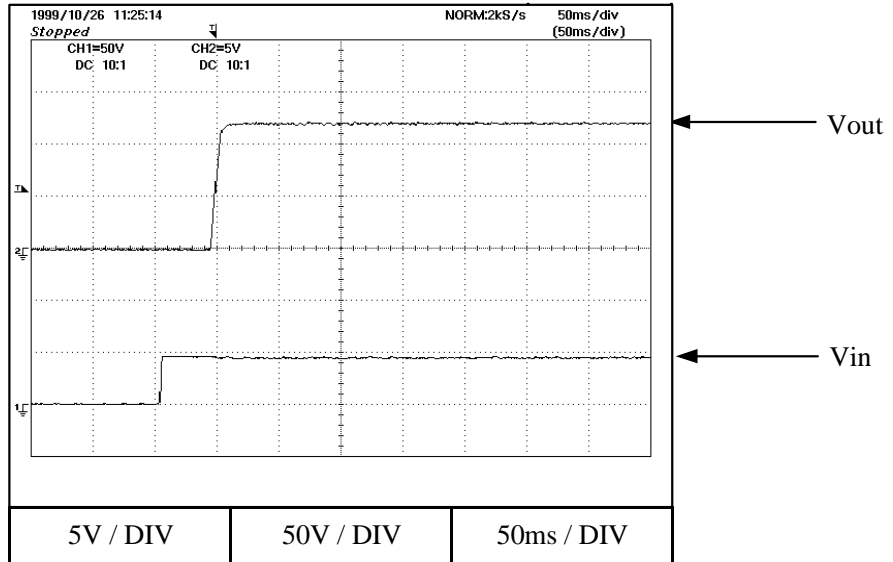
24 V



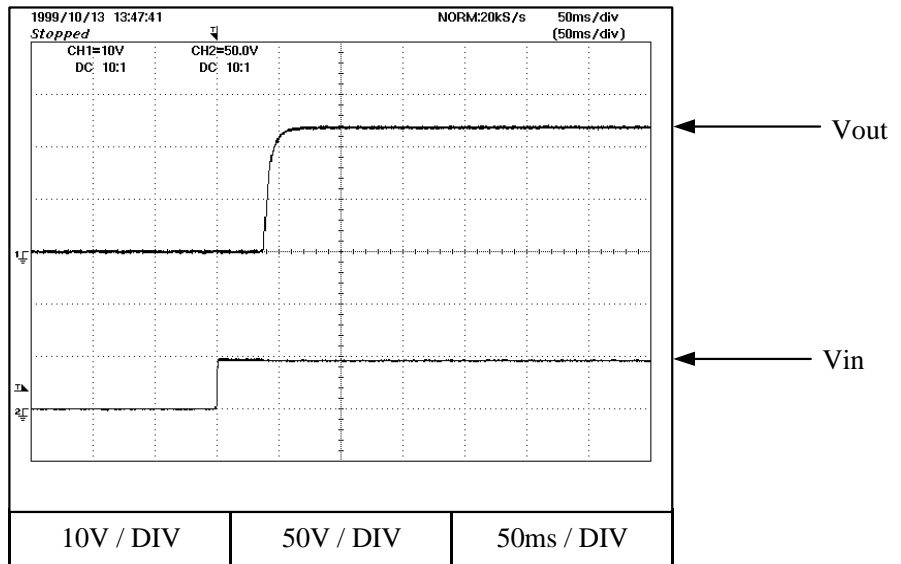
Output Rise Characteristics

Condition : $V_{in} = 48\text{ V DC}$
 $I_{out} = 100\%$
 $T_p = 25^\circ\text{C}$

12 V



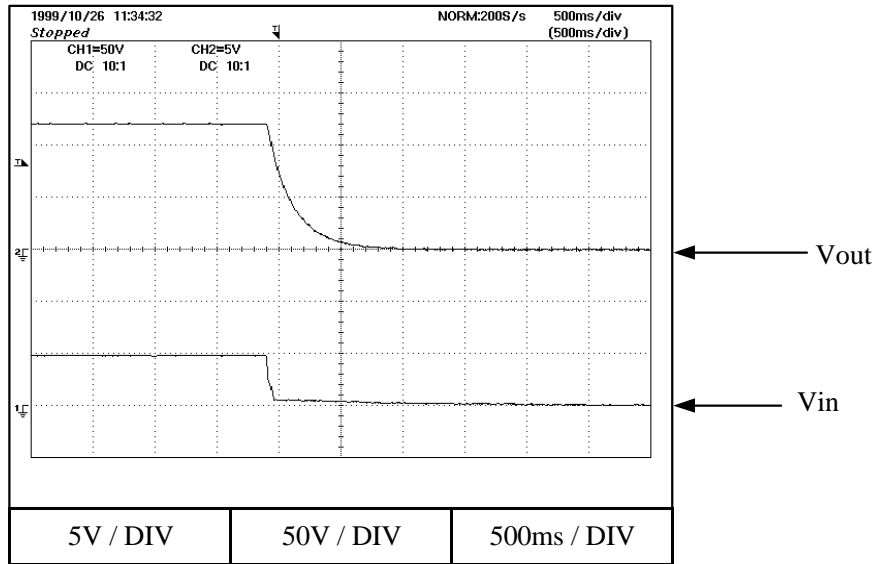
24 V



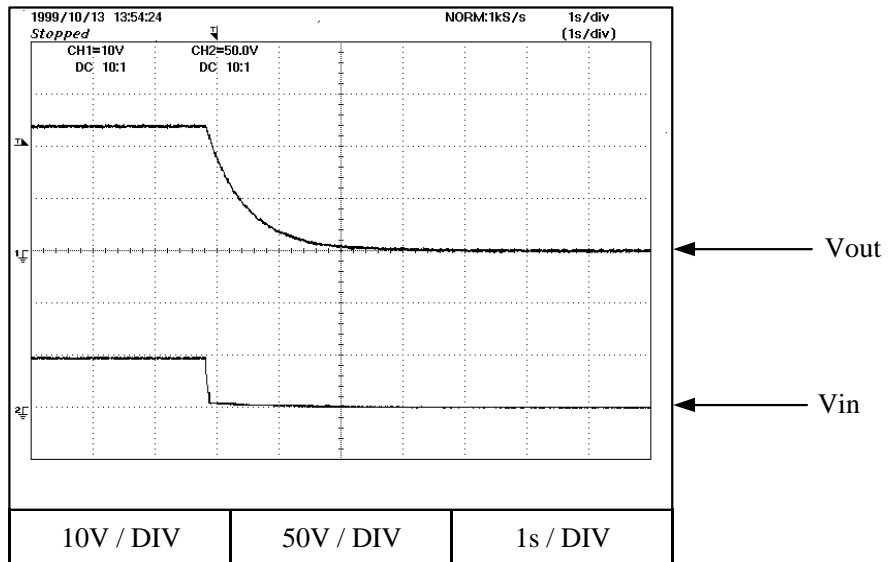
2-6 Output Fall Characteristics

Condition : $V_{in} = 48\text{ V DC}$
 $I_{out} = 0\%$
 $T_p = 25^\circ\text{C}$

12 V



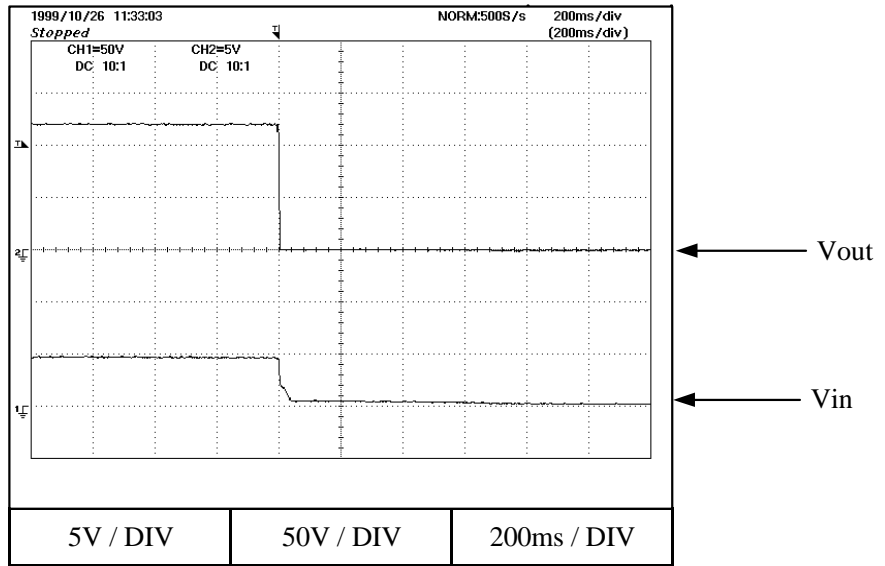
24 V



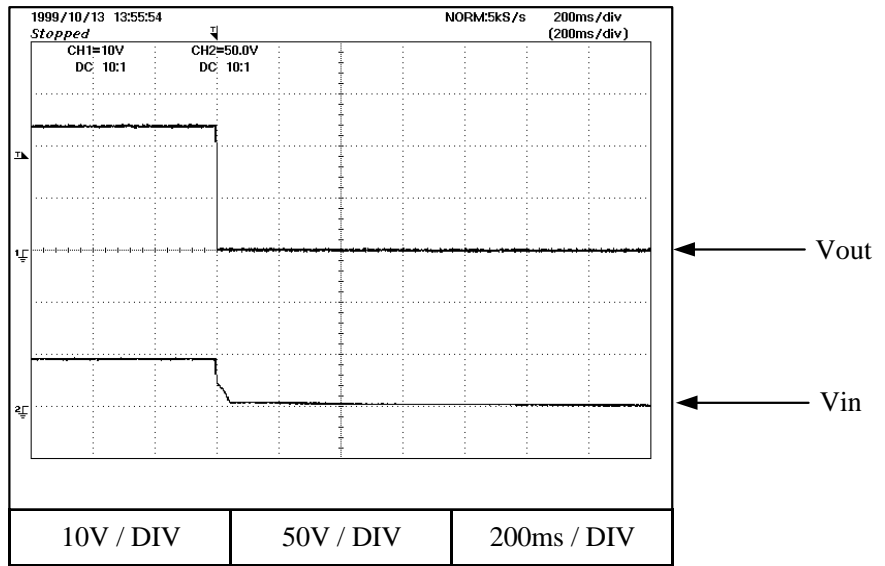
Output Fall Characteristics

Condition : $V_{in} = 48 \text{ V DC}$
 $I_{out} = 100 \%$
 $T_p = 25^\circ\text{C}$

12 V



24 V

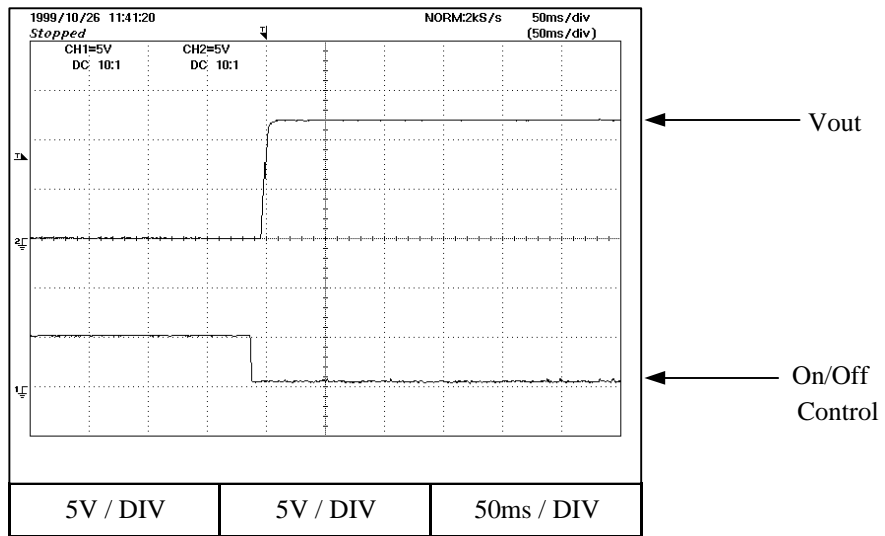


2-7 Output Rise With On/Off Characteristics

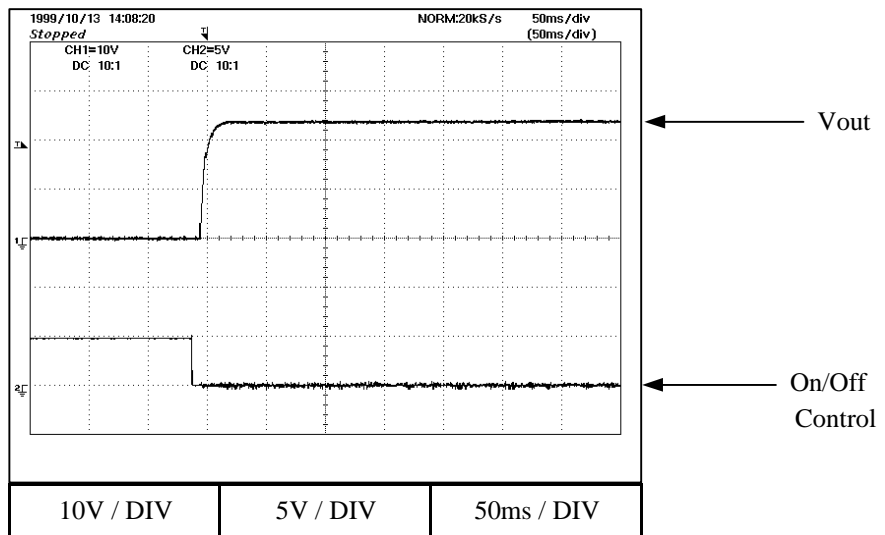
(Negative logic)

Condition : $V_{in} = 48 \text{ V DC}$
 $I_{out} = 0 \%$
 $T_p = 25^\circ\text{C}$

12 V



24 V



Output Rise With On/Off Characteristics

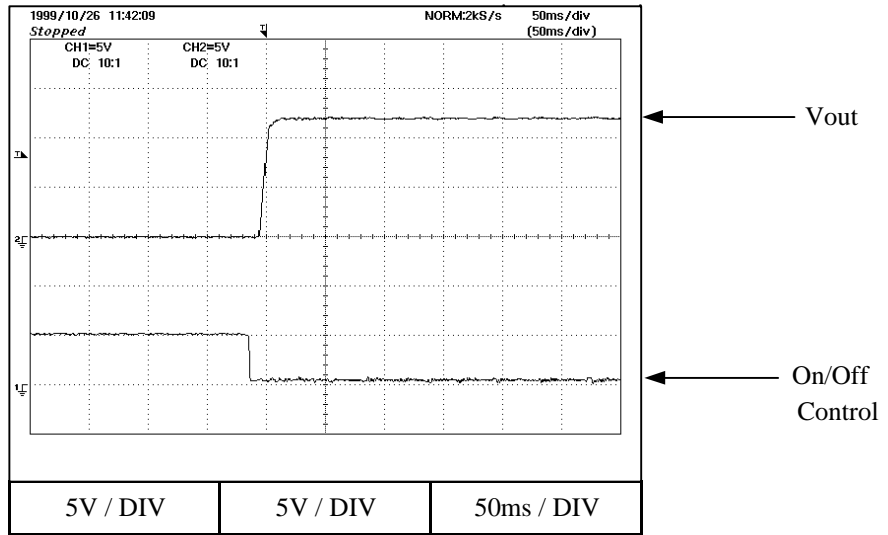
(Negative logic)

Condition : $V_{in} = 48 \text{ V DC}$

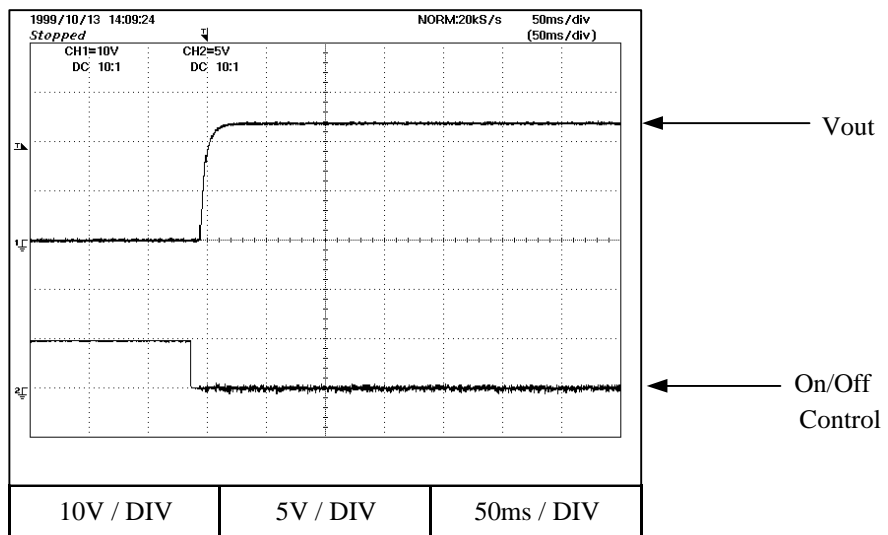
$I_{out} = 100 \%$

$T_p = 25^\circ\text{C}$

12 V



24 V

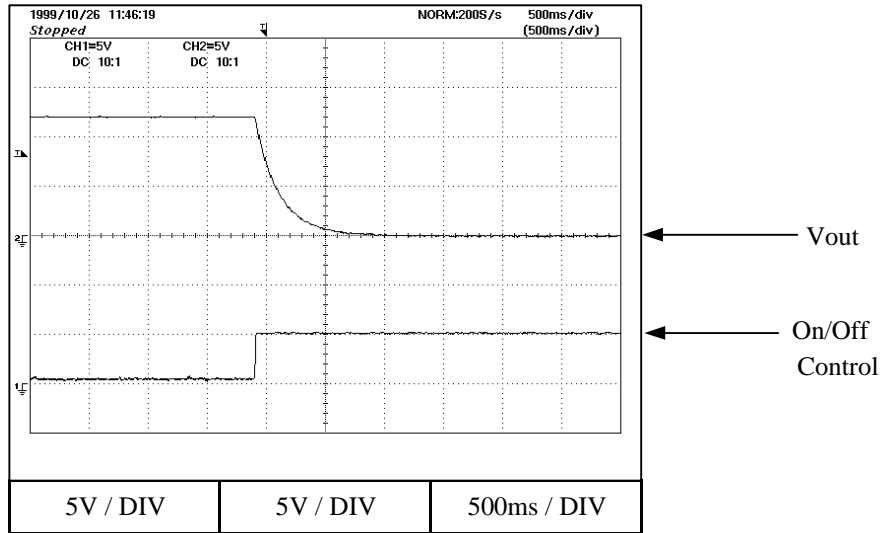


2-8 Output Fall With On/Off Characteristics

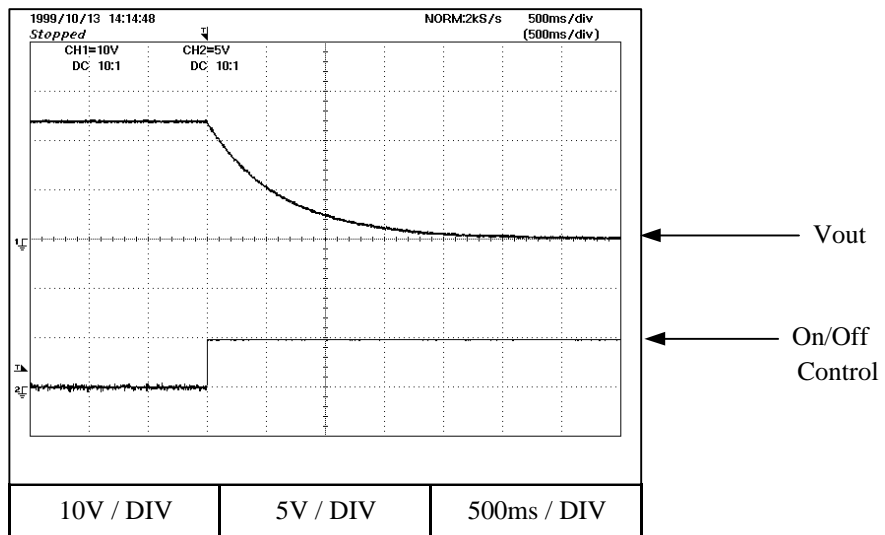
(Negative logic)

Condition : $V_{in} = 48 \text{ V DC}$
 $I_{out} = 0 \%$
 $T_p = 25^\circ\text{C}$

12 V



24 V

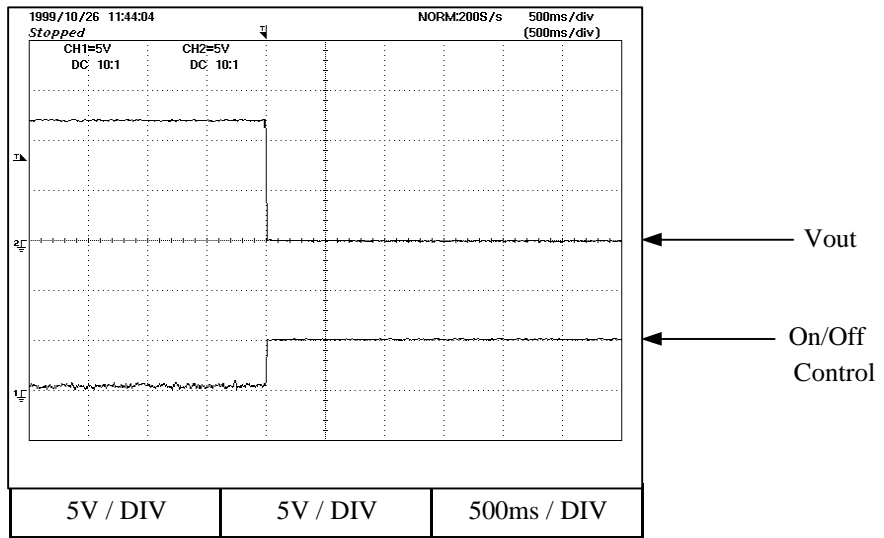


Output Fall With On/Off Characteristics

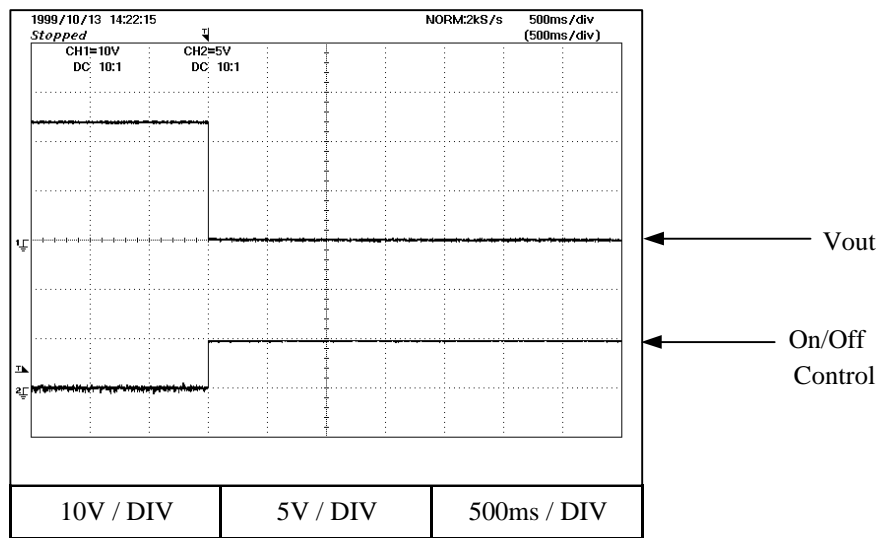
(Negative logic)

Condition : $V_{in} = 48 \text{ V DC}$
 $I_{out} = 100 \%$
 $T_p = 25^\circ\text{C}$

12 V



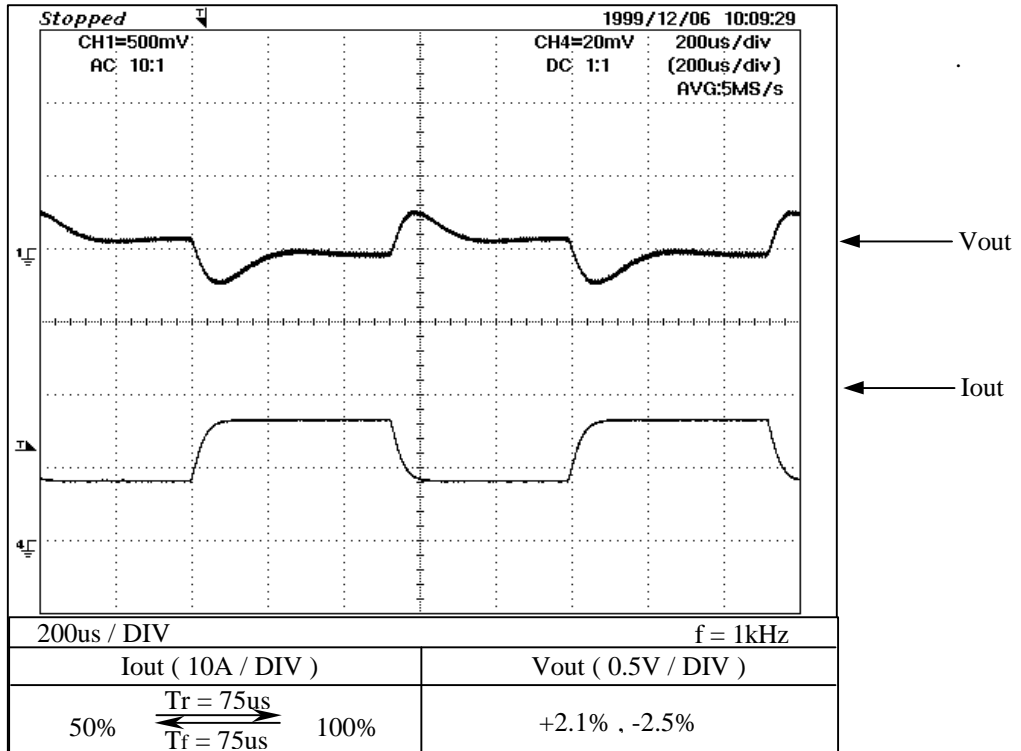
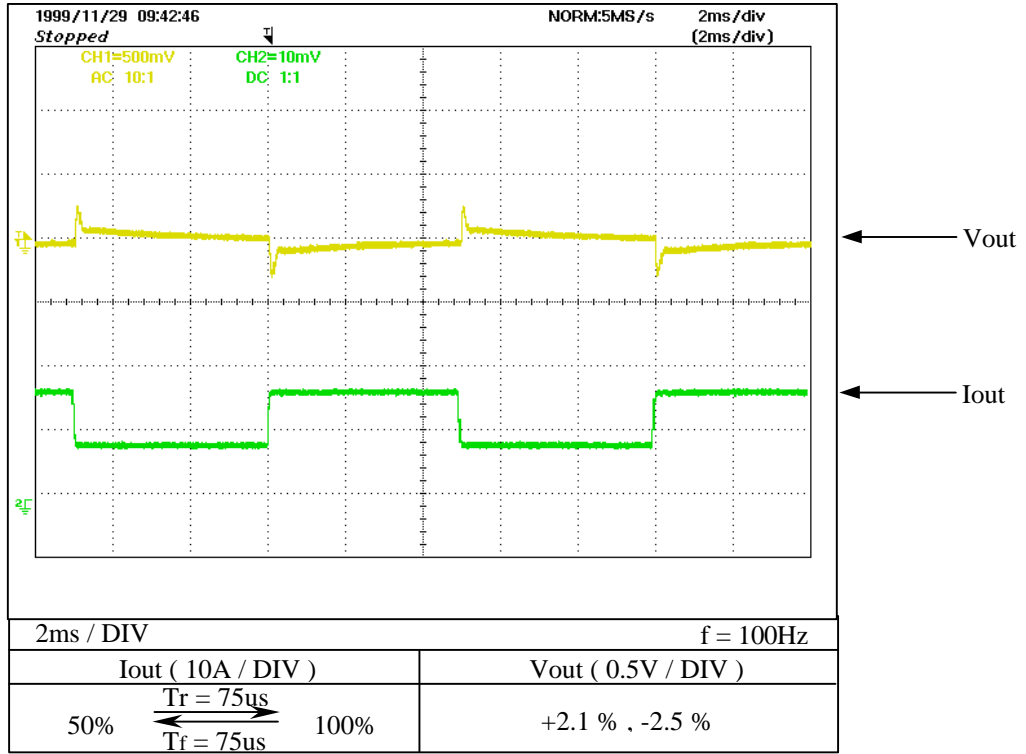
24 V



2-9 Dynamic Load Response Characteristics

Condition : $V_{in} = 48 \text{ VDC}$
 $T_p = 25^\circ\text{C}$

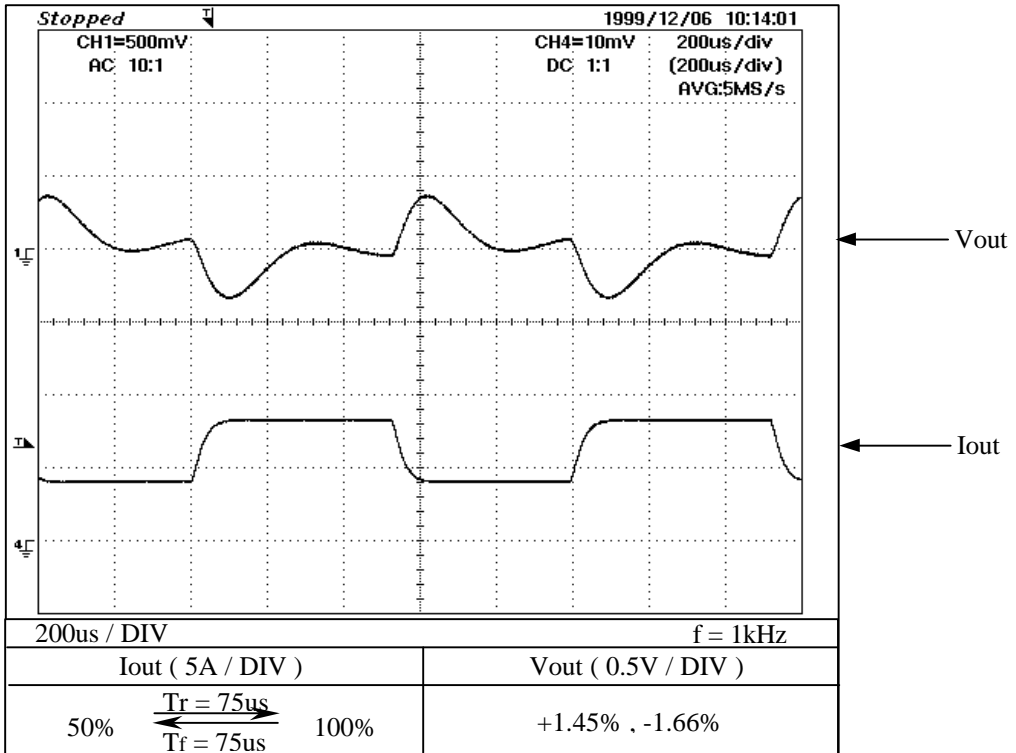
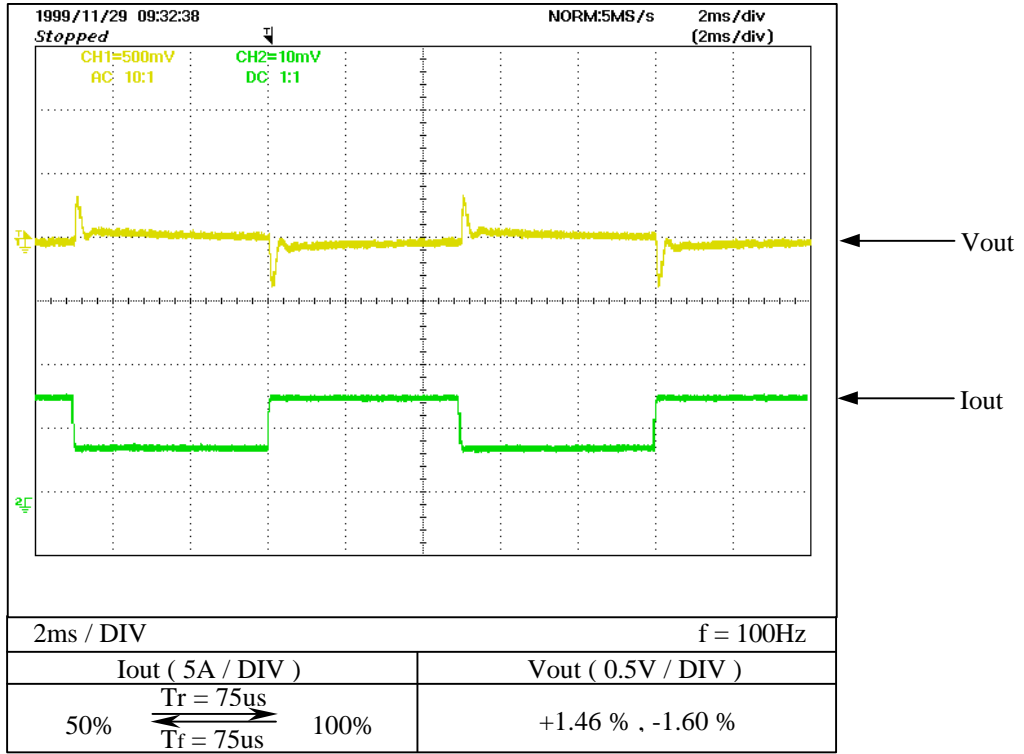
12 V



Dynamic Load Response Characteristics

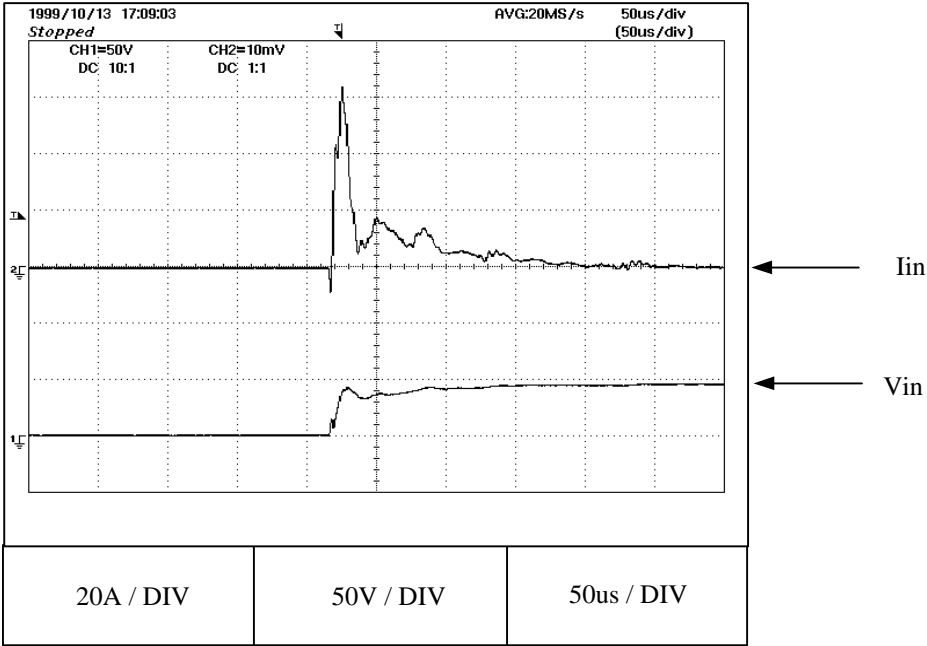
Condition : $V_{in} = 48 \text{ VDC}$
 $T_p = 25^\circ\text{C}$

24 V



2-10 Inrush Current Waveform

Condition : $V_{in} = 48 \text{ V DC}$
 $I_{out} = 100 \%$
 $T_p = 25^\circ\text{C}$

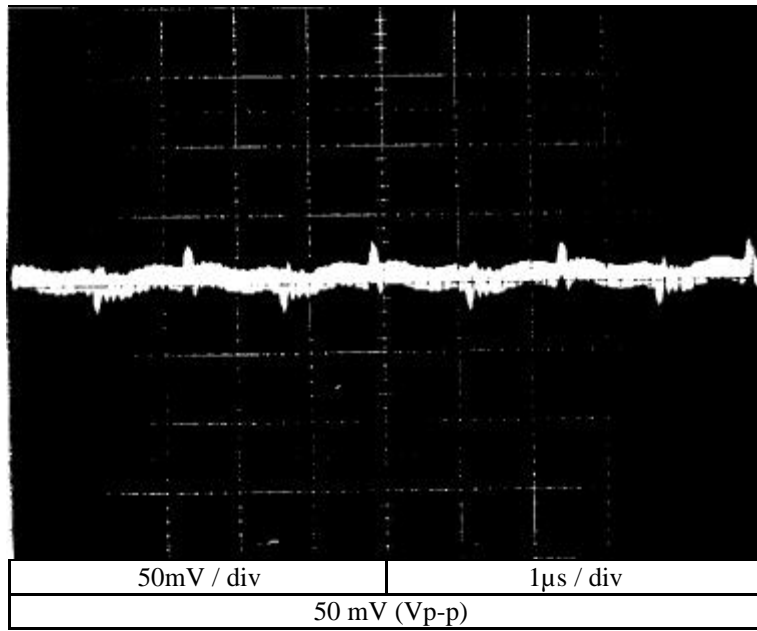


2-11 Output - Ripple & Noise Waveform

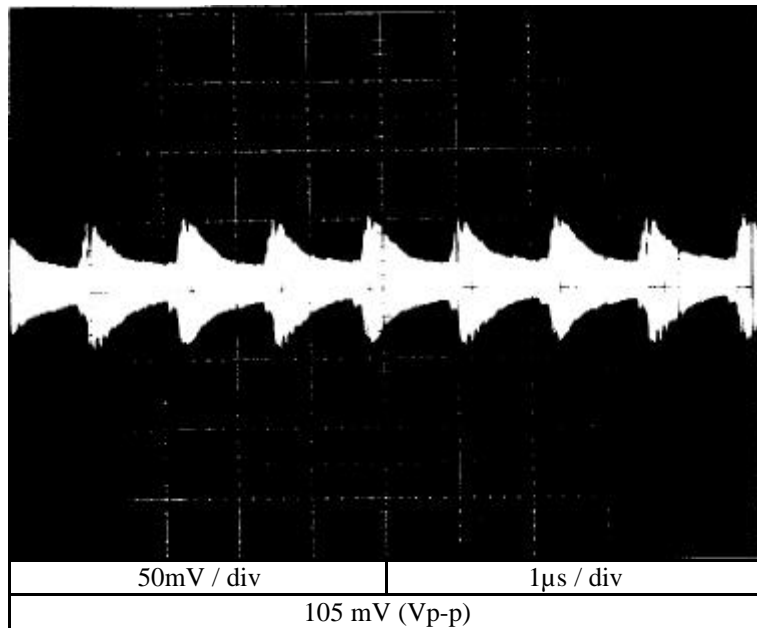
Condition : $V_{in} = 48V$ DC
 $I_{out} = 100\%$
 $T_p = 25^{\circ}C$

12 V

NORMAL MODE



NORMAL + COMMON MODE

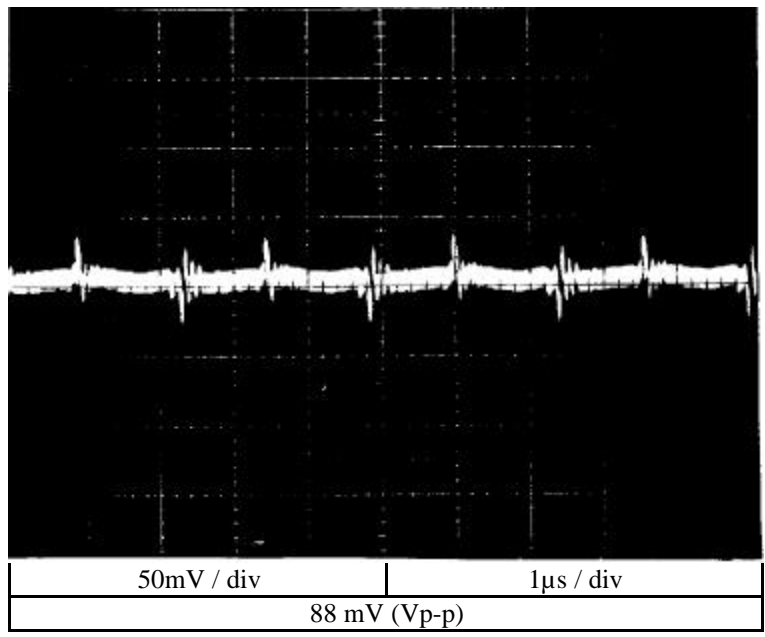


Output - Ripple & Noise Waveform

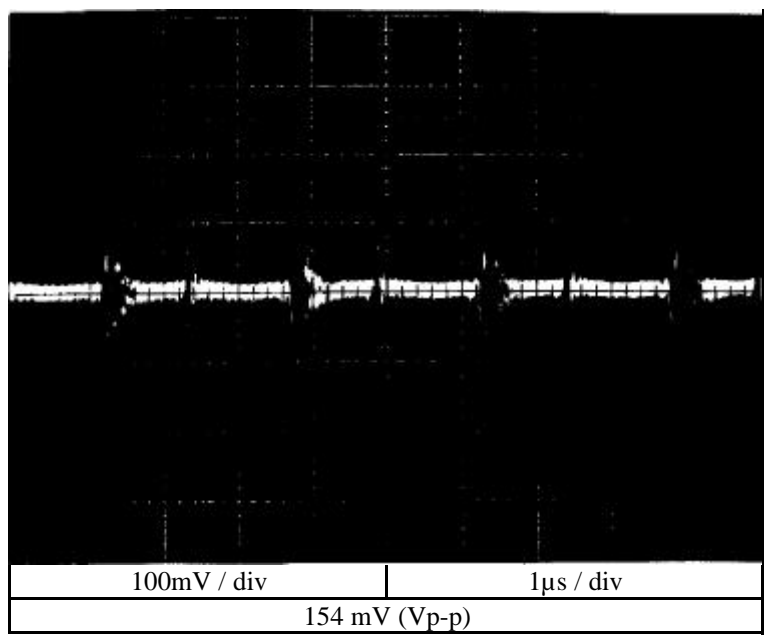
Condition : $V_{in} = 48V$ DC
 $I_{out} = 100\%$
 $T_p = 25^{\circ}C$

24 V

NORMAL MODE



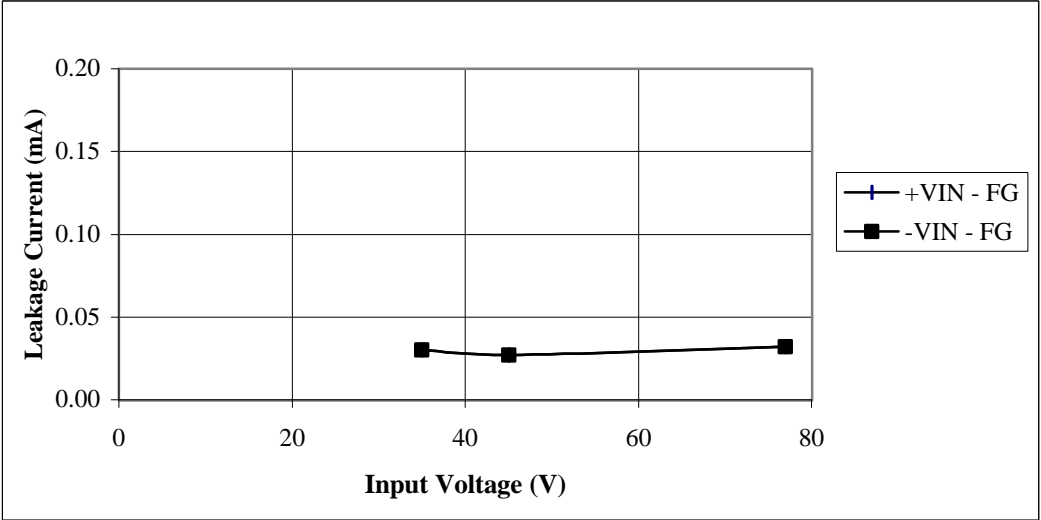
NORMAL + COMMON MODE



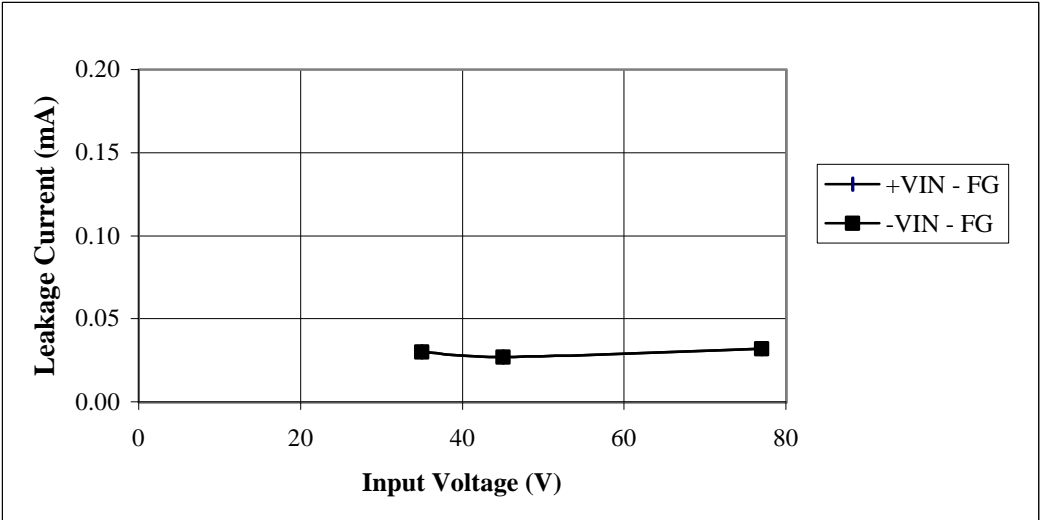
2-12 Leakage Current Characteristics

Condition : Iout = 100%
Tp= 25°C

12V



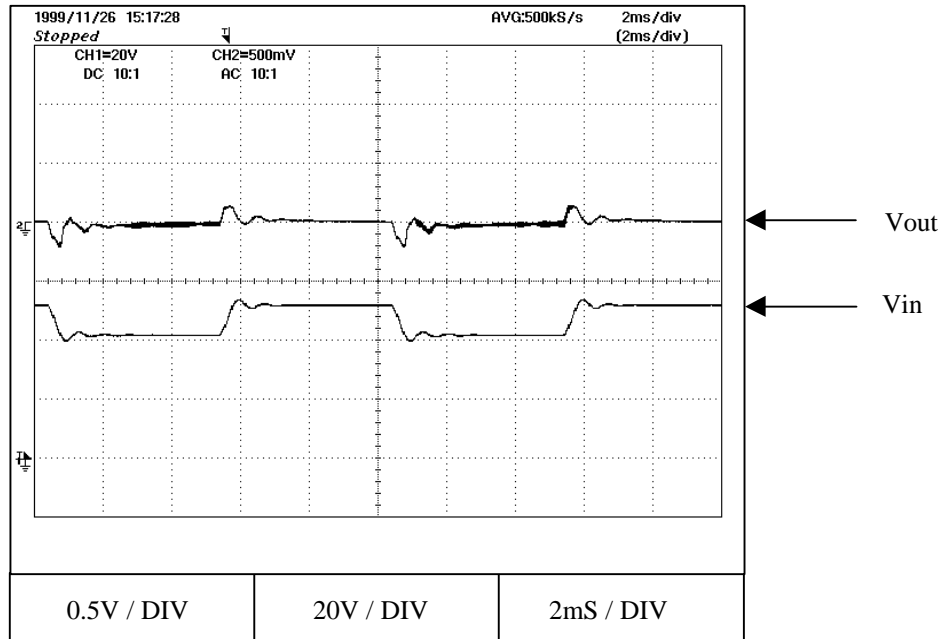
24V



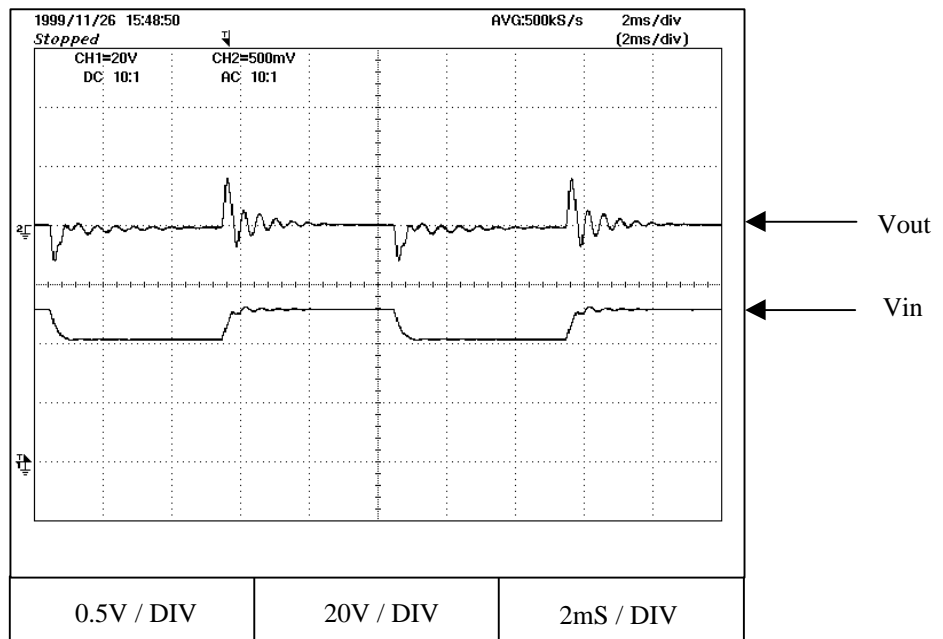
2-13 Dynamic Line Response Characteristics

Condition $V_{in} = 43 \rightleftharpoons 53VDC$
 $I_{out} = 100\%$
 $T_p = 25^{\circ}C$

12 V



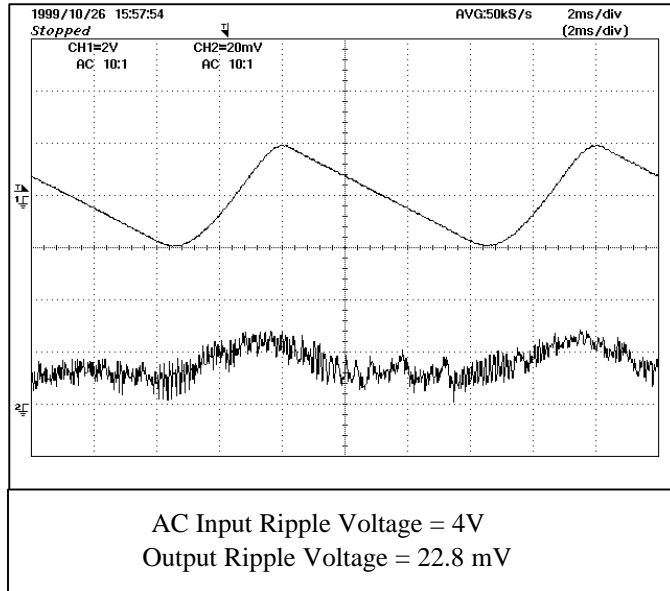
24 V



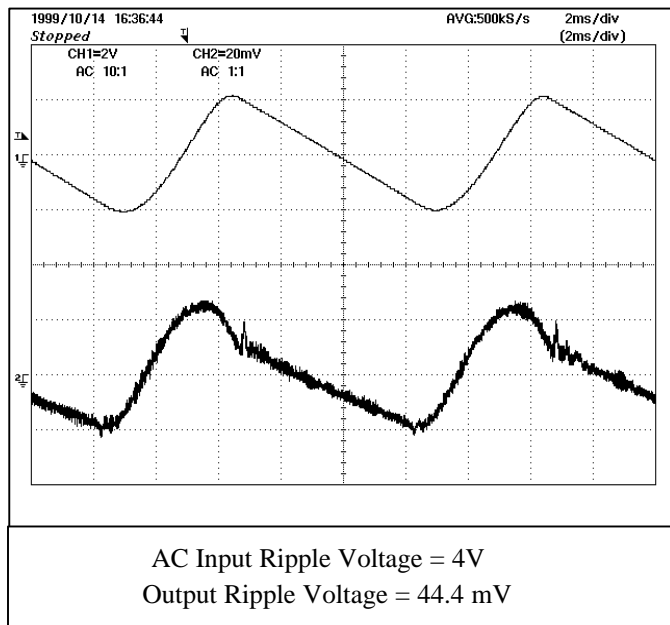
2-14 AC Input Response

Condition : Vin = 48 VDC
Iout = 100%
Tp = 25°C

12V



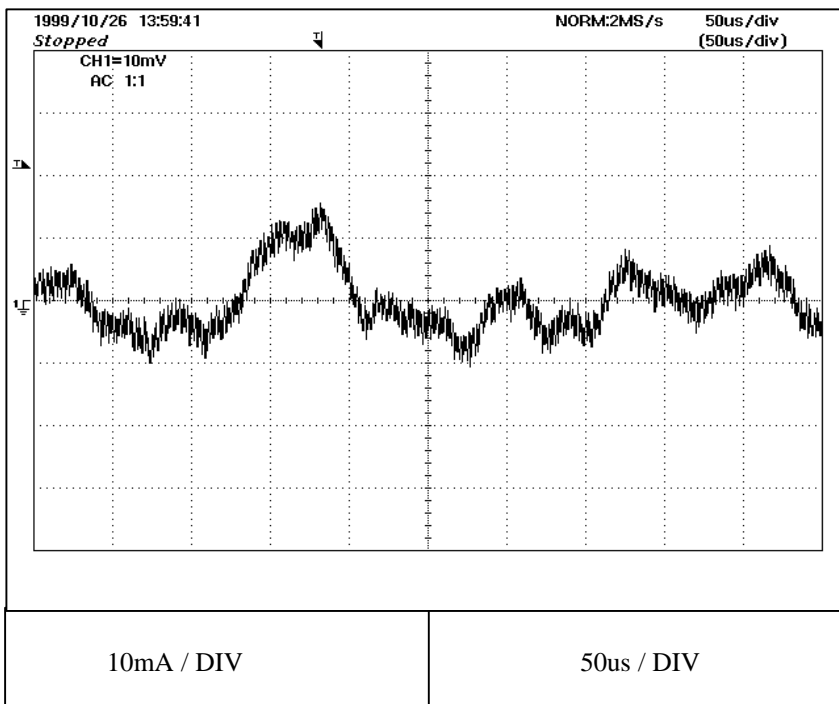
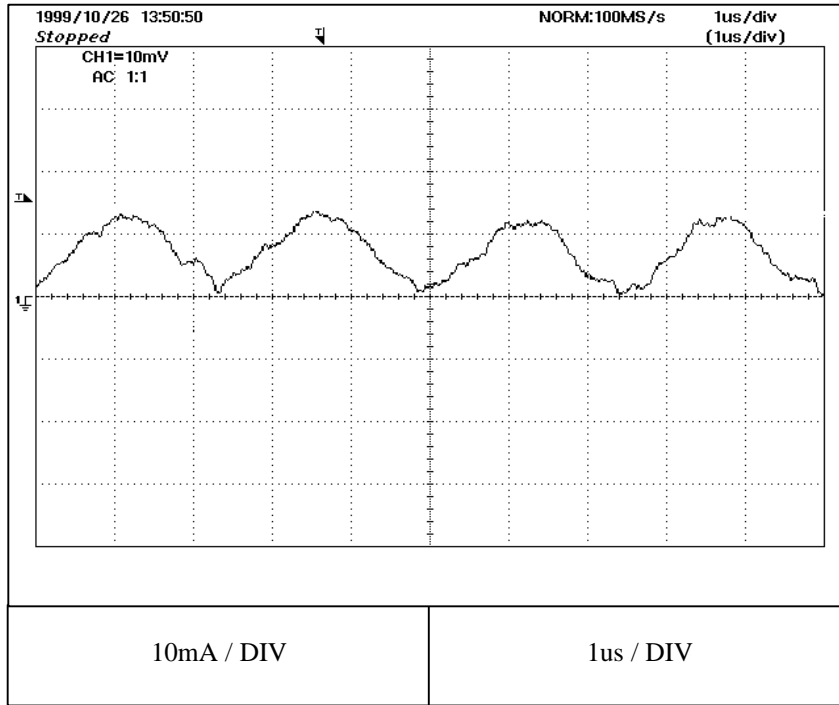
24V



2-15 INPUT REFLECTED CURRENT

Condition : $V_{in} = 48 \text{ VDC}$
 $I_{out} = 100\%$
 $T_p = 25^\circ\text{C}$

12 V



2-15 INPUT REFLECTED CURRENT

Condition : $V_{in} = 48 \text{ VDC}$
 $I_{out} = 100\%$
 $T_p = 25^\circ\text{C}$

24 V

